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	Application No.	Applicant(s)
Notice of Allowability	09/659,680	OMIZO, TAKASHI
	Examiner	Art Unit
	Prieto B	2142
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. X This communication is responsive to <u>05/28/04.</u>		
2. 🔀 The allowed claim(s) is/are <u>1-11</u> .		
3. ☑ The drawings filed on <u>11 September 2000</u> are accepted by the Examiner.		
4.		
<ol> <li>DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT</li> </ol>	FOR THE DEPOSIT OF BIOLOGIC	AL MATERIAL.
Attachment(s)	E     Nighting of Information	Optont Application (DTO 450)
<ol> <li>Notice of References Cited (PTO-892)</li> <li>D Notice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>	<ol> <li>5. ☐ Notice of Informal F</li> <li>6. ☐ Interview Summary</li> </ol>	Patent Application (PTO-152)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Da	te
Paper No./Mail Date4. ☐ Examiner's Comment Regarding Requirement for Deposit	8. 🛭 Examiner's Stateme	ent of Reasons for Allowance
of Biological Material	9.	



### Examiner's Amendment

1. An Examiner's Amendment to the record appears below. Should the changes or additions be unacceptable to Applicant, an amendment may be filed as provided by 37 C.F.R. § 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the Issue Fee.

#### Title

- 2. Pursuant to M.P.E.P. §606.01, this title has respectfully been changed to read as follows:
- -- COMPUTER SYSTEM, COMPUTER MANAGEMENT SYSTEM AND METHOD FOR MANAGING THE COMPUTER SYSTEM IN A NON-ACTIVE STATE THROUGH AN EXTERNAL TERMINAL --

#### Amendment to the Claims

3. The following examiner's amendment is solely for correcting minor noted lack of antecedent basis in claims 2, 4, 6-7, and 10-11. Authorization for this examiner's amendment was given by Burgujian, R. (Reg. No. 31,744) on August 23, 2004 to correct minor noted informality.

#### PLEASE AMEND THE CLAIMS AS FOLLOW:

- 1. (Previously Amended) A computer system managed by a terminal having a wireless communication function, comprising:
  - a CPU, connected to a first bus, which controls the computer system;
- a non-volatile memory, connected to the first bus and a second bus, which stores a basic input/output system program;
- a wireless communication interface capable of wireless communication with said terminal; and
- a system management controller connected to said wireless communication interface and the second bus, which performs system management instructed by said terminal, through wireless communication with said terminal via said wireless communication interface,

wherein the CPU accesses the non-volatile memory via the first bus when the computer system is in an active state and the system management controller accesses the non-volatile memory via the second bus when the computer system is in a non-active state.

2. (Currently Amended) The computer system according to claim 1, further comprising:

computer identification information storing means for storing inherent computer identification information of said computer system itself,

wherein said system management controller further includes coincidence detecting means for, when receiving a connecting request including said computer identification information sent from said terminal via said wireless communication interface, detecting coincidence between said computer identification information within said connecting request and said computer identification information stored in said computer identification information storing means, and

connection completion notifying means for, when said coincidence is detected by said coincidence detecting means, returning to said terminal a response indicating that said computer system is connected with said terminal, via said wireless communication interface.

- 3. (Previously Amended) The computer system according to claim 2, further comprising:
- a display used for displaying at least that said computer system is connected to said terminal,

wherein said system management controller includes a display means for displaying that said computer system is connected to said terminal on said display when a response indicating that said computer system is connected to said terminal is returned to said terminal.

- 4. (Currently Amended) A computer system managed by a terminal having a wireless communication function, comprising:
  - a CPU, connected to a first bus, which controls the computer system;
- a wireless communication interface capable of wireless communication with said terminal;
- a system management controller connected to said wireless communication interface, for performing system management instructed by said terminal, through wireless communication with said terminal via said wireless communication interface; and
- a system management bus operable even at a non-activation time of said computer system, which is directly or indirectly connected to various information storing means of said computer system,

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wherein the CPU accesses the various information storing means via the first bus when the computer system is in an active state and said system management controller includes an information access means for recovering, modifying, or reading out information on said information storing means, via said system management bus, depending on a received request for recovering, modifying, or reading out said information on said information storing means of said computer system from said terminal when the computer system is in a non-active state.

- 5. (Previously Amended) The computer system according to claim 4, further comprising:
  - a bridge connected to said system management bus; and
- a non-volatile memory writable for storing a basic input/out system program, which is connected to said bridge and said system management bus,

wherein said information access means of said system management controller gains access to said non-volatile memory via said system management bus only at said non-activation time of said computer system.

- 6. (Currently Amended) The computer system according to claim 4, further comprising:
- a first bridge to which a first bus, a second bus, and a main memory that is one of said various information storing means are connected: and
  - a second bridge mutually connecting said second bus and said system management bus,
- wherein said information access means of said system management controller accesses said main memory via said system management bus, said second bridge, said second bus, and said first bridge, under control of said CPU, at said non-activation time of said computer system.
- 7. (Currently Amended) The computer system according to claim 4, further comprising:
  - a first bridge mutually connecting a first bus and a second bus;
- a second bridge to which said second bus, said system management bus, and a disk drive that is one of said various information storing means are connected,

wherein said information access means of said system management controller gains access to said disk drive via said system management bus and said second bridge, under control of said CPU, at said non-activation time of said computer system.

8. (Previously Amended) The computer system according to claim 6,

wherein said first bridge includes an abnormal time access interface for connecting said main memory to said system management bus at said non-activation time of said computer system, and

said information access means of said system management controller accesses said main memory via said system management bus at said non-activation time of said computer system.

9. (Previously Amended) The computer system according to claim 7,

wherein said second bridge includes an abnormal time access interface for connecting said disk drive to said system management bus at said non-activation time of said computer system, and

said information access means of said system management controller accesses to said disk drive via said system management bus at said non-activation time of said computer system.

- 10. (Currently Amended) A computer management system comprising:
  - a CPU, connected to a first bus, which controls the computer management system;
- a non-volatile memory, connected to the first bus and a second bus, which stores a basic input/output system program;
  - a terminal having a wireless communication function; and
  - a plurality of computer systems capable of managing a system by said terminal,

wherein said computer management system comprises a wireless communication interface capable of wireless communication with said terminal, a system management controller connected to said wireless communication interface, for performing system management instructed by said terminal, through said wireless communication with said terminal via said wireless communication interface, wherein the CPU accesses the non-volatile memory via the first bus when the computer management system is in an active state and the system management controller accesses the non-volatile memory via the second bus when the computer management system is in a non-active state.

11. (Currently Amended) The computer management system according to claim 10, further comprising:

a local area network connecting said terminal and said plurality of said computer systems, wherein said terminal selects either wireless communication or cable communication via said local area network so as to communicate with said system managing controller of said computer management system, for managing a system of said plurality of said computer systems.

## 12. (Canceled)

## Reason for allowance

4. The following is the Examiner's statement of reason for allowance. This statement is not intended to necessarily state all the reasons for allowance or all the details why claims are allowed, nor it specifically or impliedly state that all the reasons for allowance are set forth. The primary, or important reason for allowance of the claims is the inclusion of the limitation(s) in all the claims, which is not found in the prior art references.

In this case, the prior art of record teaches various features the claimed invention substantially as claimed, including a terminal configured to manage a computer system over a wireless communication medium, the hardware structure supporting this communication and the inter-communication related functions including claimed coincidence detection by which the computer determines that it is being communicatively addressed by the terminal, thereby responding accordingly. However, the prior art of record fails to teach or suggest individually or in combination; accessing the non-volatile memory via a second bus used when the computer system is in non-active state, where the second bus is coupled to a wireless interface in the computer system for performing system management functions such as accessing the non-volatile memory remotely when instructed by the terminal, when the computer system is in non-active state, as set forth by claims 1, 4 and 10. Claimed invention as a whole is distinguishable over the prior art of record in that in substance, the invention enables a terminal to access wirelessly via a second bus a computer's non-volatile memory and manage its basic input/output system program (BIOS) stored therein, while the computer is non-active state (see specification p. 13-15 and page 10 of remarks filed 5/28/04). Claims 1-11 are allowed because of the combinations of other limitations and the limitation listed above.

5. Related co-pending applications and/or common inventor's related issued patents (US 6,260,151 & 5,520,058) have been considered for obviousness or statutory double patenting, none is warrant.

- Any comments Applicants considers necessary must be submitted no later than the payment of the Issue Fee and to avoid processing delays, should preferable accompany the Issue Fees. Such submission should be clearly labeled "Comments on Statement of Reasons for Allowance". In event of any post-allowance papers (e.g. IDS, 312 amendment, petition, etc.), Applicant is exhorted to mail papers to the Production Control branch in Publications or faxed to post-allowance papers correspondence branch at (703) 308-5864 to expedite issuing process or call PUB's Customer Service if any questions at (703) 305-8497.
- Any inquiry concerning this communication or earlier communications from the examiner should be directed to B. Prieto whose telephone number is (703) 305-0750. The Examiner can normally be reached on Monday-Friday from 6:30 to 4:00 p.m. If attempts to reach the examiner by telephone are unsuccessful, the Examiner's Supervisor, Jack B. Harvey can be reached on (703) 305-9705. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3800/4700.

Any response to this action should be mailed to:

Box Issue Fee Commissioner of Patents and Trademarks Washington, D.C. 20231

or faxed to:

(703) 746-4000, (Issue Fee and any Publication fee/payments)

or:

(703) 305-8283 (for checking on receipt of payment w/Publication)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington VA, Fourth Floor (Receptionist), further ensuring that a receipt is provided stamped "Technology Center 2100".

B. Prieto

Patent Examiner